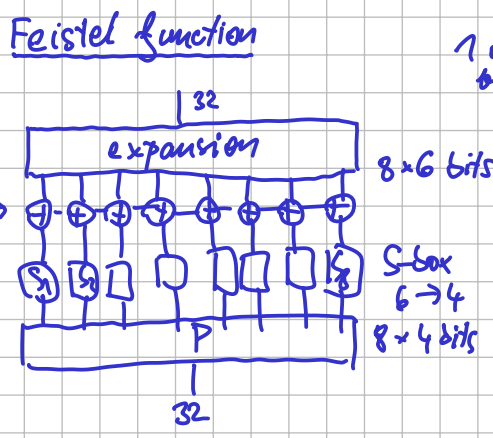
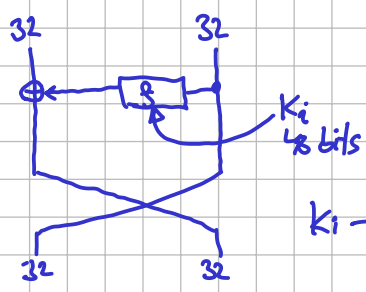
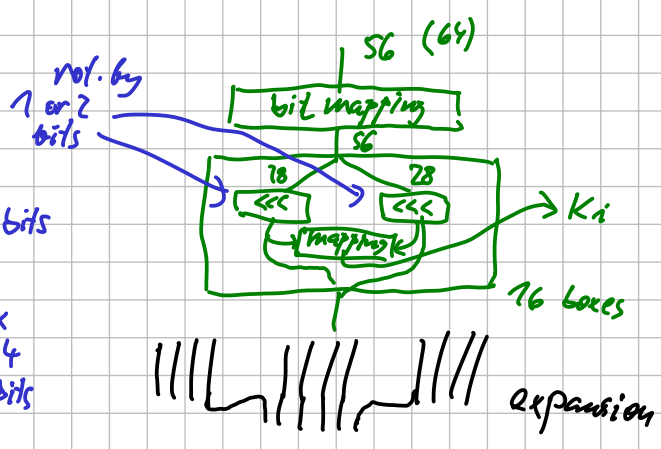


# DES Feistel network with 16 rounds

64-bit blocks, 56-bit keys



## Key schedule



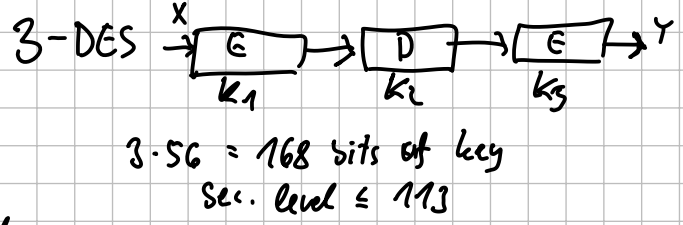
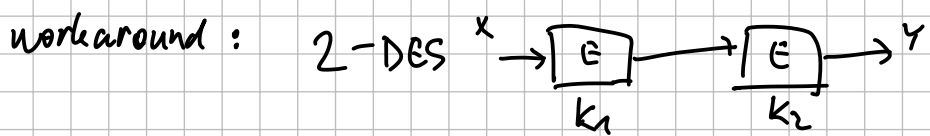
## Critique of DES

- weak keys: if  $K = 0^{56}$ , then  $\forall i, K_i = 0^{48}$ , so  $\oplus$  in  $f$  has no effect  $\rightarrow$  all rounds are identical

$\hookrightarrow K = 1^{56}$  So  $E_K = D_K$

$E_{K^{-1}}(x) = E_K(x)$

- too short key: brute-force attacks 2012 ... FPGA-based machine which cracks DES in 26 hours
- too short blocks: in  $2^{32}$  blocks we have block collision



key size  $2 \cdot 56 = 112$  bits  
But: security level  $\leq 57$  (exercise)

? group structure?  
 $\forall K_1, K_2 \exists K$   
 $E_{K_1} \circ E_{K_2} = E_K$

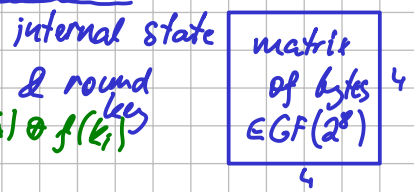
- too much secrets
- attacks on structure ....  $2^{47}$  chosen plaintexts

## AES (Advanced Encryption Standard)

- 1997 NIST public competition, 15 candidates, 3 rounds of evaluation
- Rijndael became AES in 2001

128-bit blocks, 128 or 192 or 256 bit keys  
 $\downarrow$  10 rounds     $\downarrow$  12     $\downarrow$  14

### Structure: SPN with linear step



inversion in GF + affine transf.

Round of decryption:

- Inv Mix
- Add Rk (fixed)
- Inv Byte Sub
- Inv Shift Rows

Round:

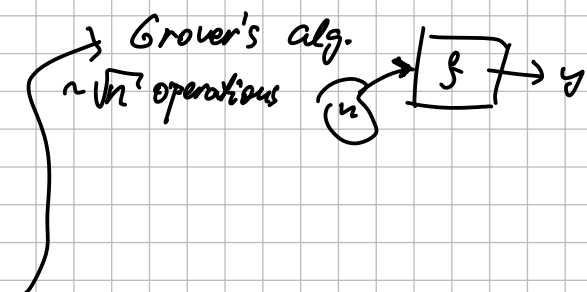
- S ByteSub (16 identical S-boxes)
- P Shift Rows
- Mix Columns: linear transform on every column
- Add Round Key ... XOR with  $K_i$

$m_1(x) = \text{mix} \left( \begin{pmatrix} s(x), 0, 0, 0 \end{pmatrix} \right)$   
 $m_2(x) = \text{mix} \left( \begin{pmatrix} 0, s(x), 0, 0 \end{pmatrix} \right)$   
 $m_3(x) = \text{mix} \left( \begin{pmatrix} 0, 0, s(x), 0 \end{pmatrix} \right)$   
 $m_4(x) = \text{mix} \left( \begin{pmatrix} 0, 0, 0, s(x) \end{pmatrix} \right)$

$\text{mix} \left( \begin{pmatrix} s(x), s(x), s(x), s(x) \end{pmatrix} \right) = m_1(x) \oplus m_2(x) \oplus m_3(x) \oplus m_4(x)$   
 $[2^8] \rightarrow [2^{32}]$

# Critique of AES:

- simple algebraic structure
- small margin in # rounds
- byte-aligned
- 128-bit key: quantum attacks
- 128-bit blocks: block collisions in  $\sim 2^{64}$  blocks  
work-around: change keys after  $2^{32}$  blocks (64GB)



Brute force:  
 $\sim n$  operations

## Other finalists in AES comp.:

- Serpent: 128b blocks, 128-256b keys, 32 round SPN + linear
- Twofish:  $\parallel$ , 16-round Feistel net with key-dependent S-boxes

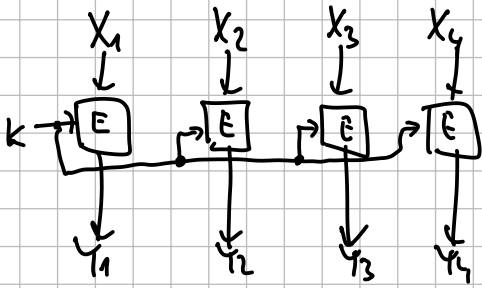
## Use of Block Ciphers, Padding



## Block Cipher Modes of Operation



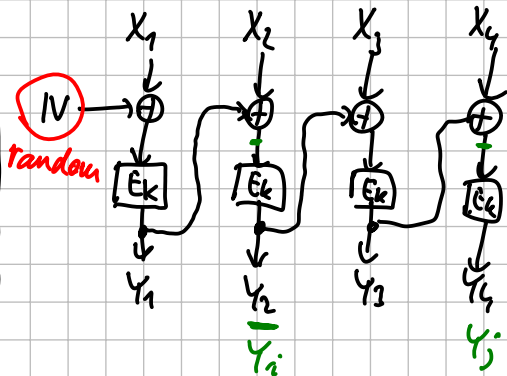
### ECB: Electronic Code Book



AVOID AT ALL COSTS!

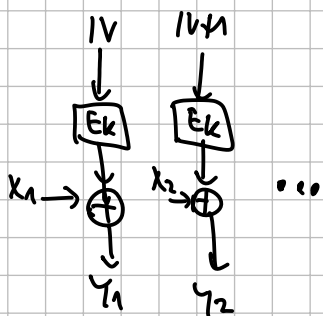
- no IV  $\rightarrow$  reveals equality
- reveals  $X_i = X_j \Leftrightarrow Y_i = Y_j$
- flip a bit in  $Y_i \Rightarrow$  destroy  $X_i$
- swap  $Y_i \leftrightarrow Y_j \Rightarrow X_i \leftrightarrow X_j$

### CBC: Cipher Block Chaining



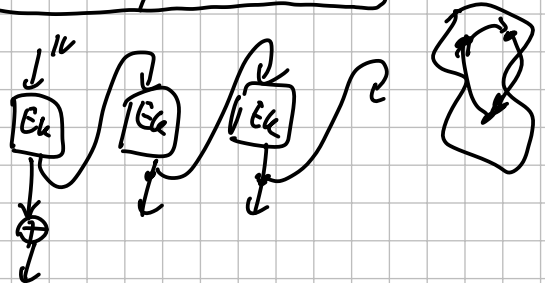
- requires random IV
- bit flip in  $Y_i \rightarrow$  destroys  $X_i \rightarrow$  flip in  $X_{i+1}$
- $Y_i \leftrightarrow Y_j$   
 $X_{i+1} \oplus \text{ed with } Y_i \oplus Y_j$   
 $X_{j+1} \text{ --- } \text{---}$   
 $X_i \leftarrow X_j \oplus Y_{i-1} \oplus Y_{j-1}$   
sim. for  $X_j \leftarrow X_i$
- Proof of security wrt. CPA (Chosen plaintext) for not-too-long messages ( $\sqrt{\text{block space size}}$  blocks)

### CTR: Counter



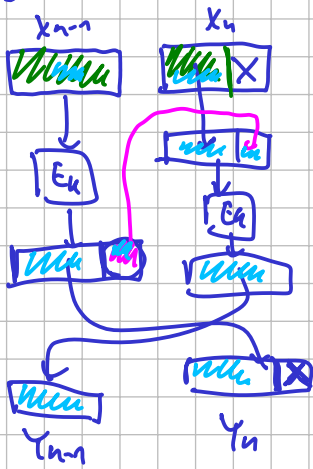
- a stream cipher  $\rightarrow$  no padding needed
- MUST NOT REPEAT IV
- bit flip in  $Y_i \rightarrow$  bit flip in  $X_i$
- random access
- parallelizable

### OFB: Output Feedback



# Ciphertext Stealing

for ECB:



for CBC

