

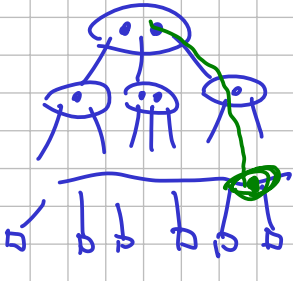
(a,b)-tree

height  $\sim \log n$

w.c. complexity

Find  $O(\log n)$

Ins/Del  $O(b \cdot \log_a n)$



external nodes

$a \geq 2$   $b \geq 2a-1$

between  $a$  and  $b$  children  
 $a-1$   $b-1$  keys

Amortized Analysis

Warmup seq. of  $m$  Inserts to an initially empty tree

Thm: Total # of modified nodes is  $O(m)$ .

Proof: # modifications =  $O(m) + O(\# \text{splits})$

initial step

the rest

# splits  $\leq$  # nodes at the end  $\leq$

# keys at the end =  $m$

General case: Seq. of  $m$  Inserts and Deletes starting with an empty tree.

Thm: # modifications is  $O(m)$  for  $b \geq 2a$ .

we will prove for  $b = 2a$ .

Proof: Use potential technique.

$\Phi := \sum_{v \text{ node}} f(\# \text{keys in } v)$

$f: \mathbb{N} \rightarrow \mathbb{N}$

$a-1$  to  $2a-1$   
 underfull overfull

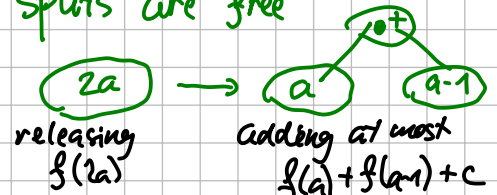
initially  $\Phi = 0$   
 always  $\Phi \geq 0$

idea: split/merge has  $O$  amort. cost (real cost  $\leq 1$ )

Requirements on  $f$ :

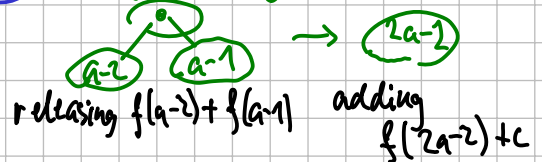
①  $\exists c : \forall k | f(k) - f(k-1) \leq c$

② splits are free



$f(2a) \geq f(a) + f(a-1) + c + 1$

③ merges are free



$f(a-2) + f(a-1) \geq f(2a-2) + c + 1$

Use this  $f$ :

$k$	$a-2$	$a-1$	$a$	...	$2a-2$	$2a-1$	$2a$
$f(k)$	2	1	0	...	0	2	4

Verify: ① OK with  $c=2$  ②  $4 \geq 0+1+2+1$

③  $2+1 \geq 0+2+1$

Am. cost of Insert =  $O(c) + O$  per Split  
 initial insertion

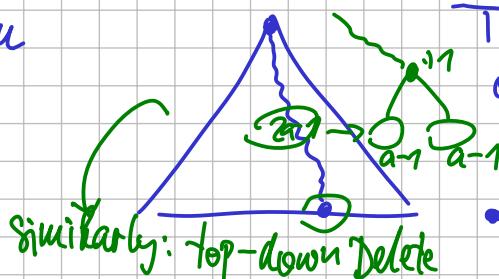
Am. cost of Delete =  $O(c) + O$  per Delete +  $O(c)$   
 incl. del. stealing key

always a constant

Q.E.D.

Parallel program

need locking



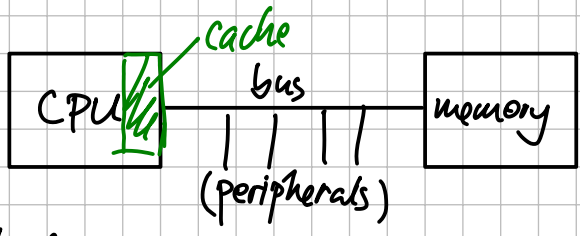
similarly: top-down Delete

Top-down Insert for  $b \geq 2a$ :

- when finding the place for the new node, split nodes with  $2a-1$  keys
- when adding the new item, we are sure we have space for it

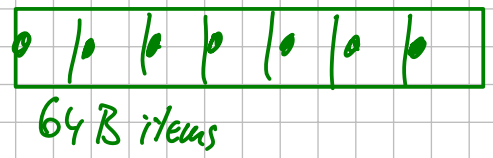
pre-emptive splitting

# Caching



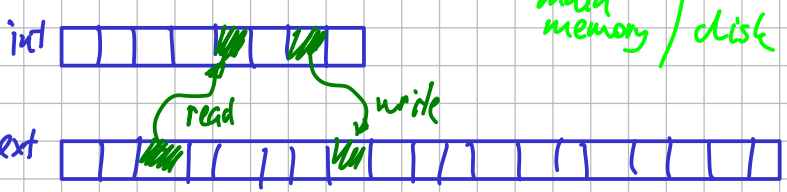
typical parameters:  
 L1 cache 32 KB  
 L2 cache 256 KB  
 L3 cache 4 MB  
 } in 64B blocks

1 cycle of 1 GHz clock = 1 ns =  $10^{-9}$  s  
 speed of light =  $3 \cdot 10^8$  m/s  
 in 1 cycle, light travels 30 cm



## I/O model (ext. mem. model)

memory is of limited size, we can compute here (RAM-like)  
 internal cache / main mem.  
 external main memory / disk — potentially infinite, split to blocks



instructions for transfer of blocks between int. and ext. mem.

parameters:  $B$  = block size  
 $M$  = size of int. memory

Complexity measures: time, space, I/O (communication)

↳ typically count just reads

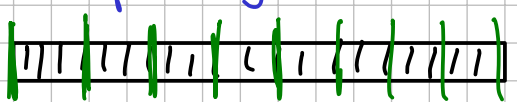
## Caching models

- int. and ext. memory, params  $B$  and  $M$
  - program addresses data in ext. mem.
  - there is a cache controller which brings the data to int. mem. as needed
- ↳ we assume that the controller is optimal (it minimizes the I/O complexity)

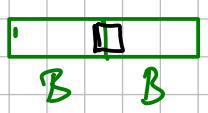
Cache-aware - program knows  $B, M$

Cache-oblivious - it doesn't

## Warm-up: Array Scan size $N$



stored in  $\lceil N/B \rceil$  consecutive blocks



I/O model: #reads =  $\lceil N/B \rceil$   
 we need  $M \geq B$

Cache-aware model: #reads =  $\lceil N/B \rceil$

Cache-oblivious model: we cannot ensure alignment

#reads =  $\lceil N/B \rceil + 1$  in w.c.

Conclusion: #reads  $\in O(\lceil N/B \rceil + 1)$

for arbitrarily small  $B$  for infinitely many  $(N, B)$   $O(N/B) = O(N)$